

EAST-109881472.wsu:1

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Active

L1: (47) (((sense adj2 (amp amplifier)) with (shar\$3 switch\$3 select\$3

L5: (7) "200200976"

L10: (2) I9 and ((ISO isolation) adj3 signal)

L9: (302) (((tim\$3 adj3 circuit) with input) same (sense adj2 (amp ampl

L14: (7144) (timing adj2 circuit) with (input and output)

L15: (5793) ((timing adj2 circuit) with input) with ((timing adj2 circuit

L17: (4) L15 with (ISO (isolat\$3 adj2 signal))

L16: (4) L15 with (ISO (isolation adj2 signal))

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Details operator OR

(((tim\$3 adj3 circuit) with input) same (sense adj2 (amp amplifier))) same
decoder same output same (array block bank)

365 Item 368 Item 369 Item 370 Item 371

	U	I	Document ID	Issue Date	Pages	Title	Current OR	Current XRef	Retrieval C	Inventor	S	C	P	3	
31	<input type="checkbox"/>	<input type="checkbox"/>	US 6411561 B1	20020625	36	Semiconductor device including multi-chip	365/230.03	365/222; 365/51;		Ayukawa, Kazushige et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
32	<input type="checkbox"/>	<input type="checkbox"/>	US 6392950 B2	20020521	37	Semiconductor device including multi-chip	365/230.03	365/189.04; 365/222;		Ayukawa, Kazushige et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
33	<input type="checkbox"/>	<input type="checkbox"/>	US 6349072 B1	20020219	21	Random access memory device	365/233	365/189.05; 365/230.08		Origasa, Kenichi et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
34	<input type="checkbox"/>	<input type="checkbox"/>	US 6259629 B1	20010710	91	Nonvolatile semiconductor memory device	365/185.22	365/185.18		Seki, Koichi et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
35	<input type="checkbox"/>	<input type="checkbox"/>	US RE37176 R	20010515	14	Semiconductor memory	365/203	365/205		Kajigaya, Kazuhiko et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
36	<input type="checkbox"/>	<input type="checkbox"/>	US 6232793 B1	20010515	40	Switched backgate bias for FET	326/34	257/827.112; 326/83;		Arimoto, Kazutami et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
37	<input type="checkbox"/>	<input type="checkbox"/>	US 6198679 B1	20010306	15	Semiconductor memory device	365/203	365/207; 365/233		Nakasu, Teruhiko et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
38	<input type="checkbox"/>	<input type="checkbox"/>	US 6181600 B1	20010130	94	Nonvolatile semiconductor memory device	365/185.18	365/185.22; 365/185.24		Seki, Koichi et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
39	<input type="checkbox"/>	<input type="checkbox"/>	US 6157576 A	20001205	94	Nonvolatile semiconductor memory device	365/185.29	365/185.21; 365/185.22		Seki, Koichi et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
40	<input type="checkbox"/>	<input type="checkbox"/>	US 6108793 A	20000922	55	Semiconductor device having timing-stabilization circuit	713/400	713/500; 713/502		Fujii, Yasuhiro et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
41	<input type="checkbox"/>	<input type="checkbox"/>	US 6028805 A	20000222	14	Volatile memory and embedded dynamic random access memory	365/222	365/230.03		Higuchi, Takashi	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
42	<input type="checkbox"/>	<input type="checkbox"/>	US 6016273 A	20000110	91	Nonvolatile semiconductor memory device	365/185.22	365/185.18		Seki, Koichi et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>

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